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Gloria Sikora

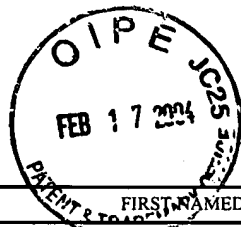
(Depositor's name)



(Signature)

February 12, 2004

(Date)



APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/808,097	03/13/2001	Peter Rabkin	00939A-079310US	4045

TITLE OF INVENTION: TRANSISTOR WITH ULTA-SHORT GATE FEATURE AND METHOD OF FABRICATING THE SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	03/01/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
MULPURI, SAVITRI	2812	365-199000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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TOWNSEND and TOWNSEND
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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hynix Semiconductor, Inc.

Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent);

☐ individual ☐ corporation or other private group entity ☐ government

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Feb. 12, 2004

02/19/2004 HLE444 00000074 201430 09808097

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